CERTIFICATE OF MAILING

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January 5, 2005

(Date)

Eric M. Nelson, Reg. No. 43,829

Alexandria, VA 22313-1450, on



Docket No.: IMEC92.001DV1

INFORMATION DISCLOSURE STATEMENT

Applicant

Brockmeyer, et al.

App. No

10/766,159

Filed

January 27, 2004

For

OPTIMIZED DATA TRANSFER AND

STORAGE ARCHITECTURE FOR MPEG-4 MOTION ESTIMATION ON

MULTI-MEDIA PROCESSORS

Examiner

Jose L. Couso

Art Unit

2621

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is an Information Disclosure Statement by Applicant (PTO/SB/08 equivalent) listing four (4) references to be considered by the Examiner. Also enclosed are four (4) copies of foreign patent references and/or non-patent literature as listed on the Information Disclosure Statement.

This Information Disclosure Statement is being filed within three months of the filing date, with an RCE or before receipt of a first office action after an RCE and no fee is required.

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Eric M. Nelson

Registration No. 43,829

Attorney of Record

Customer No. 20,995

(619) 235-8550

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	Application No.	10/766,159
INFORMATION DISCLOSURE	Filing Date	January 27, 2004
STATEMENT BY APPLICANT	First Named Inventor	Erik Brockmeyer
STATEMENT BY APPLICANT	Art Unit	2621
(Multiple sheets used when necessary)	Examiner	Jose L. Couso
SHEET 1 OF 1	Attorney Docket No.	IMEC92.001DV1

JAN O7 7		NON PATENT LITERATURE DOCUMENTS	
A APPIPALA	No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
	1	Committee Draft, Information technology-coding of audio-visual objects: Visual, N1902 "International organization for standardization, ISO/IEC/JTC1/SC29WG11," Nov 1997.	
	2	Kulkarni, C. et al., "Cache Optimization for Multimedia Compilation on Embedded Processors for Low Power," accepted for Intnl. Parallel Proc. Symp. (IPPS), Orlando FA, April 1997.	
	3	Leonardo Chiariglione - Convenor, "MPEG Workplan," http://drogo.cselt.stet.it/mpeg/workplan.htm	
	4	VLSI Technology, Inc., "Datasheet book: 0.6-micron 5-V core mixed 5V / 9V I/O cell-based libraries," VLSI Technology, Inc., San Jose, US, Aug. 1994.	

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Examiner Signature

Date Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.